EMB-6

EXPANSION MOTHEREGARD MODULE

OPERATOR'S GUIDE

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Specifications Subject To Change Without Notice

INTRODUCTION

The EMB-6 is a 6 card slot Expansion Motherboard which is used to extend the AIM-65 Microcomputer Bus, enhancing in this manner its capabilities. The EMB-6 plugs directly to the AIM-65 expansion connector. Different Modules can be used as add-on boards to this Microcomputer, such as the CRT-80 video display, FP-950 Floppy Disk controller, MB-64, 64K dynamic RAM, A/D converters or any other modules supporting the EXORciser Bus.

FEATURES

- * EXORciser Bus compatible
- * 6 card slots
- * Fully decoded to select portions of memory in 4K increments by DIP switches.
- * Fully Buffered.
- * Supports DMA.
- * Straight extended from the expansion connector on the AIM-65.

SPECIFICATIONS

Expansion capacity

64K bytes, 6 Modules

Interfaces:

AIM-65 to EXORciser Bus

Size:

12" wide by 8.25" long

Expansion Connector:

44 pins on 0.156" centers

EXORciser Connectors:

86 pins on 0.156" centers

Operating temperature

0°C to 70°C.

Expansion Board Power

+5 Vdc + 5% @ 0.3 Amp.

Requirement (Module only)

Logic Levels

		Symbol	Min	Max	Conditions
Power	DO D7 40 415	vcc	4.75V	5.25V	
inputs:	DO-D7, AO-A15, SYNC, R/W, Ø2	v _{IL}		0.8V	$I_{IL} = -0.4 \text{ mA}$
Ouputs:	D0-D7	$v_{ ext{OL}}$	2.0V	VCC 0.5V	$I_{IH} = 40 \text{ uA}$ $I_{OL} = 48 \text{ mA}$
•	A0-A15, SYNC, Ø2	v _{OH} v _{OL}	2.4V	0.5V	$I_{OH} = -10 \text{ mA}$
	no Als, Sino, V2	AOH AOF	2.4V	0.51	$I_{OL} = 48 \text{ mA}$ $I_{OH} = -5.2 \text{ mA}$

TABLE 1

EMB-6 ADDRESS SELECTION

Silk Screen Label	Address Range (Hex)
0	0000 to OFFF
1	1000 to 1FFF
2	2000 to 2FFF
3	3000 to 3FFF
4	4000 to 4FFF
5	5000 to 5FFF
6	6000 to 6FFF
7	7000 to 7FFF
8	8000 to 8FFF
9	9000 to 9FFF
A	A000 to AFFF
В	BOOO to BFFF
С	COOO to CFFF
D	DOOO to DFFF
E	E000 to EFFF
P .	FOOO to FFFF

Note that the on-board 4K RAM and ROM (Monitor, BASIC, Assembler, I/O) on the AIM-65 occupy locations \$0000 to \$0FFF and \$B000 to \$FFFF respectively. They should be physically removed if external access to AIM-65 at these locations is intended. For instance, if and external RAM memory board is desired, which starts at location \$0000, the RAM devices on the AIM-65 should be removed before enabling the switch labeled "0" on the expansion board.

A jumper (H2) is provided to optionally connect the $\emptyset 2$ clock on Pin J to pin L on the 6 card slots for some boards supporting the EXORciser bus, which require this clock on pin L.

INSTALLATION

The EMB-6 is shipped with the card guides dismounted for easy way of handling. The following procedure should be followed to set the Module in operation with the AIM-65.

- a) Install the 12 card guide on AIM-65 with the screws provided, one on each side of the EXORciser bus connector.
- b) Remove all power to the AIM-65
- c) Attach the expansion connector to the expansion connector on the AIM-65.
- d) Jumper the power lines from the power block connector on the AIM-65 to the power block on the EMB-6.
- e) Install the modules on the EXORcier Bus connectors and make sure they are installed correctly with the component side facing the AIM-65.
- f) Configure address selection switches $0-\bar{r}$ to enable external Data access according to Table 1.
- g) Apply power to both the AIM-65 and the EMB-6.

The polarity of the EXORciser data bus in the EMB-6 is determined by the buffers used on U4, U5. The boards are shipped with 8T26 devices on these sockets to make the data bus invert. The reason for this is that most modules that plug on the EXORciser bus come with buffers to invert the data. Should the data bus not invert on the EMB-6 then the 8T26 buffers on U4, U5 are to be replaced for 8T28 devices. Note that if the replacement of the 8T26's for 8T28's is done, then the buffers on some of the modules that plug on the EXORciser bus may have to replaced so the data is not inverted. Modules such as RAM memory do not require this type of change since what goes in is what comes out (inverted or not).

DMA

The $\overline{\text{DMA}}$ line is used to tri-state the address, data and R/W buffers on the $\overline{\text{EMB-6}}$. The $\overline{\text{DMA}}$ signal when high enables the address, data and R/W signal from the AIM-65. When $\overline{\text{DMA}}$ signal is pulled low these signals are tristated to allow other controller modules to take over the bus for fast transfers of data or any other application. The $\overline{\text{DMA}}$ has a pullup resistor which keeps the signal high when not used.

CONNECTORS

The EMB-6 connects to the AIM-65 through the 44 pin expansion connector. The pin assignment is given on Table 2. The EXORciser connectors pin assignment is shown on Table 3.

		TABLE 2	
	EMB-6 TO AIN	I-65 CONNECTOR (P1)
1	SYNC	A	10
2	RY	В	A1
3	Ø1	С	A2
4	ĪRQ	D	A3
2 3 4 5	S.O.	E	A4
6	NMI	F	A5
6 7	RES	H	A6
8	D7	J	A7
9	D6	K	A8
10	D5	L	A9
11	D4	M	A10
12	D3	· N	A11
13	D2	P	A12
14	D1	R	A13
15	DO	S	A14
16	-12V	T	A15
17	+12V	U	SYS Ø2
18	CS8	V	SYS R/W
19	CS9	W	R∕W
20	CSA	X	TEST
21	+5V	Y	Ø2
22	GND	Z	RAM R/W

TABLE 3 EXORciser BUS PIN ASSIGNMENT (J1-J6) PIN NO. NAME PIN NO. NAME +5 VDC +5 VDC B C +5 VDC +5 VDC 2 3 4 +5 VDC +5 VDC IRQ NMI D E F RES 5 6 7 R/W H J GND Ø1 Ø2 GND 9 GND GND K L M N (Ø2)* 10 -12 VDC -12 VDC 11 12 13 P 14 RSTUVWXYZIAIBICIDIEIFIHIJIKILIMINIPIRISITIUIPIWIXIY BANK SELECT 15 16 +12 VDC +12 VDC 17 18 19 20 21 22 23 SYNC 24 25 GND GND S.O. 26 27 GND RDY 28 D3 D7 D2 D6 A14 D1 29 30 D5 D0 D4 31 32 A15 33 34 A13 A12 A10 35 A11 Α9 36 8A 37 A7 A6 A5 38 Α4 A2 39 A3 40 A0 Al 41 GND GND GND 42 GND GND 43 GND

^{*} PIN L is $\emptyset 2$ when jumper H2 is installed.